



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/609,311  
Filing Date ..... June 26, 2003  
Inventor ..... Donald L. Yates  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 1756  
Examiner ..... Kathleen Duda  
Attorney's Docket No. .... MI22-2236  
Title ..... Semiconductor Processing Patterning Methods

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Reference - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56.  
Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies  
of any cited U.S. patents or U.S. published applications are included herewith. No  
admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

A check in the amount of \$180.00 is enclosed to cover the fee specified under 37  
C.F.R. § 1.17(p).

Respectfully submitted,

Date: 11/12/07

Robert C. Hyta  
Reg. No. 46,791

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